


## Faculty Profile

1	<b>Name of the Faculty:</b>	Mr. Jamdade Abhijit Suryakant			
	<b>Designation</b>	Assistant Professor			
	<b>Department</b>	Electronics and Telecommunication Engineering			
	<b>Email Address</b>	abhijeet.jamdade@dnysanshree.edu.in			
	<b>Date of Joining the Institution</b>	01/07/2014			
2	<b>Academic Qualification: M.E. (Electronics)</b>		<b>Specialization: - Digital System</b>		
	<b>Highest Qualification:</b>	Masters in Engineering	<b>Year of Highest Qualification:</b>	2013	
3	<b>Experience in Years: 18 Years</b>				
	Teaching	15 Years	Professional	03 Years	
4	<b>Area(s) of Research</b>	Embedded System Design			
5	<b>Number of Publications in reputed journals:</b>				
	National Journals Papers	0			
	International JournalsPapers	2			
6	<b>Number of Conference Papers Published</b>			5	
7	<b>Number of Books/Book chapter's Published</b>			0	
8	<b>Number of Patents published/awarded</b>			0	
9	<b>Consultancy Work and Social Contributions:</b>				
	No				
10	<b>Department Responsibilities:</b>				
	Project Coordinator, Class Teacher - Final Year BTECH. Guardian Teacher of two batches				
11	<b>Institute Responsibilities:</b>				
	MATLAB Coordinator				
12	<b>University Responsibilities:</b>				
	No				
13	<b>Awards &amp; Honors: Active SPOC, NPTEL, Appreciation be Texas Instruments</b>				
14	<b>Personal Details:</b>				
	Birth Date	11/12/1983			
	Gender	Male	Marital Status	Married	
	Permanent Address	Shree Aditya Nagari, Flat No. C-4, Krishananagra, Satara- 415003			
	Correspondence Address	Shree Aditya Nagari, Flat No. C-4, Krishananagra, Satara- 415003			

Signature of Faculty: